

FIG.2

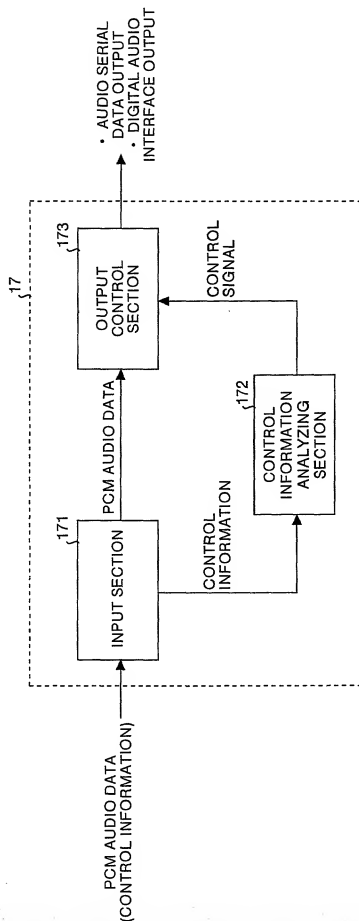


FIG.3

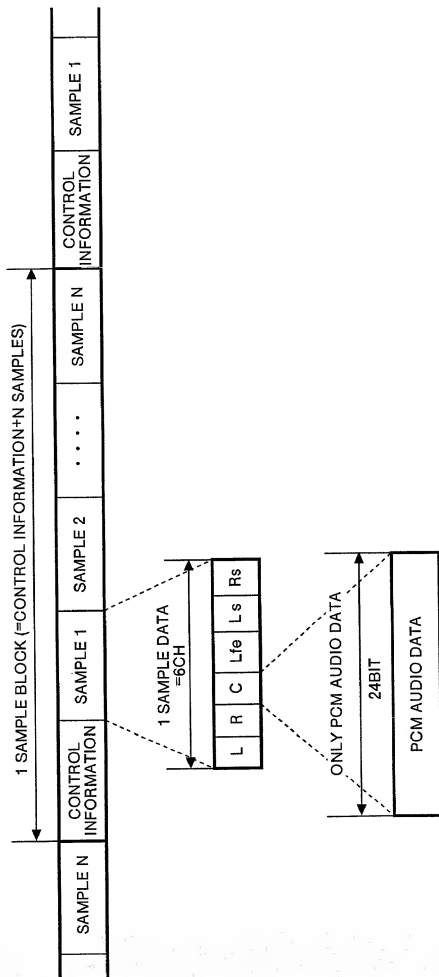


FIG.4

CONTROL INFORMATION	
OUTPUT CONTROL INSTRUCTION	OUTPUT CHANNEL NUMBER
OUTPUT SAMPLE NUMBER	DOWN SAMPLE INSTRUCTION
DATA OUTPUT WORD LENGTH	OUTPUT CHANNEL STRUCTURE
DISTRIBUTION SPECIFYING INFORMATION	

FIG.5

24BIT					
0	7 8		15 16		23
C	dw	ch_num	sample_num		
bitlen		ch_asgn slot1		ch_asgn slot2	
ch_asgn slot3		ch_asgn slot4		ch_asgn slot5	
ch_asgn slot6		ch_asgn slot7		ch_asgn slot8	

FIG.6

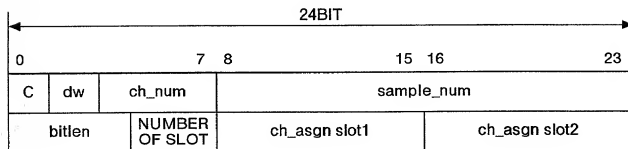


FIG. 7

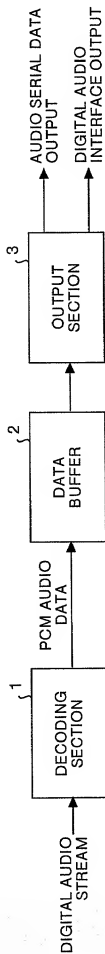


FIG. 8

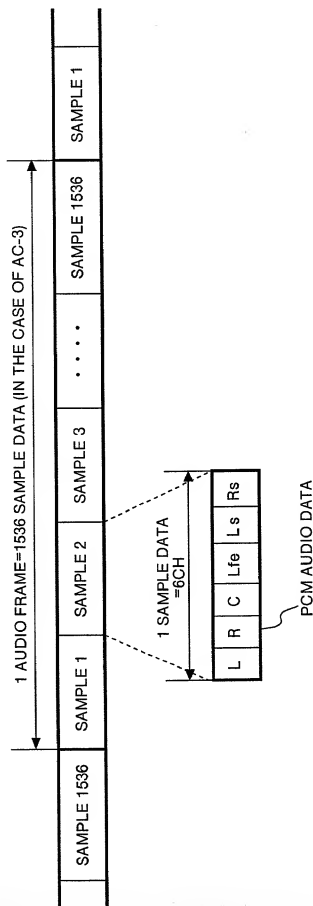


FIG.9

